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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/523,379

12/14/2005

Mark J Childs

GB 020124

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09/29/2008

PHILIPS INTELLECTUAL PROPERTY & STANDARDS

P.O. BOX 3001

BRIARCLIFF MANOR, NY 10510

EXAMINER

MCCOMMAS, STUART S

ART UNIT

PAPER NUMBER

2629

MAIL DATE

DELIVERY MODE

09/29/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed 9/11/2008 have been fully considered but they are not persuasive.

On pages 10-11 of Applicant's remarks, Applicant argues that the Koyama and Sanford combination would be inoperable because the transistor Q302 would be moved to be directly connected to the anode of OLED 320.

The Examiner respectfully disagrees. The test for obviousness is not whether the features of a secondary reference may be bodily incorporated into the structure of the primary reference; nor is it that the claimed invention must be expressly suggested in any one or all of the references. Rather, the test is what the combined teachings of the references would have suggested to those of ordinary skill in the art. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981). In the instant case, the Examiner is merely suggesting that by directly connecting the additional transistor 1409 from Koyama to the anode of the EL element 1405 to supply any voltage including a holding voltage to the OLED precise gradation display can be achieved and picture quality can be improved. The Examiner is not suggesting that the transistor Q302 from Sanford be reconfigured to be directly connected to the anode of the OLED but that the concept of directly connecting a transistor to the anode of the display element for the purpose of controlling voltage is well known and would have been obvious at the time the invention was made.

Conclusion

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2. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stuart McCommas whose telephone number is (571)270-3568.

The examiner can normally be reached on Monday-Friday 9 AM to 5 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Alexander Eisen can be reached on (571)272-7687. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Sumati Lefkowitz/
Supervisory Patent Examiner, Art Unit 2629

Stuart McCommas
Patent Examiner
Art Unit 2629

SSM